



GP
ELECTRONICS

GPT038N15NTB
150V N-Channel MOSFET

Product Summary

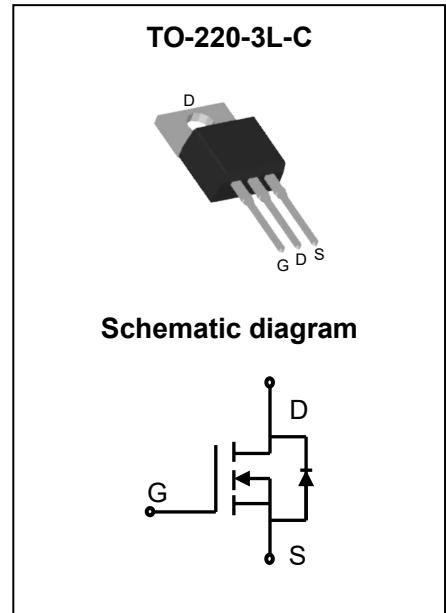
$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
150V	3.8mΩ@10V	200A

Feature

- Split Gate Trench Technology
- Low $R_{DS(ON)}$
- Low Gate Charge
- Low Gate Resistance
- 100% UIS Tested

Application

- Power Switching Application
- Motor Driving
- Power Management



Package Marking and Ordering Information

Part Number	Package	Marking	Packing	Reel Size	Tape Width	Qty
GPT038N15NTB	TO-220-3L-C	T038N15N	Tube	-	-	50pcs

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

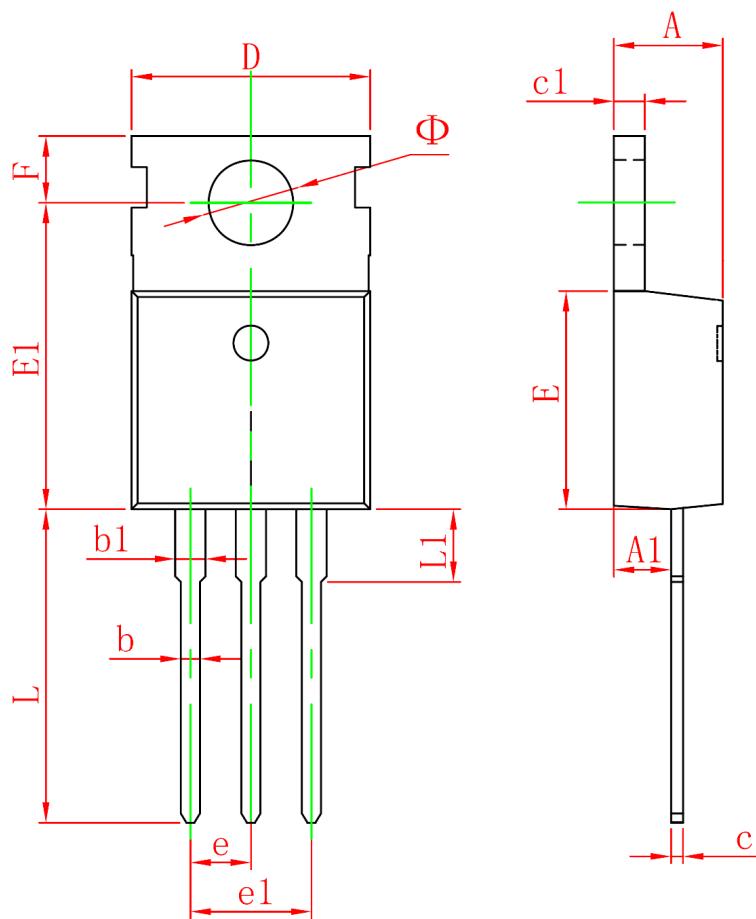
Parameter		Symbol	Value	Unit
Drain - Source Voltage		V_{DS}	150	V
Gate - Source Voltage		V_{GS}	± 20	V
Continuous Drain Current ¹	$T_c = 25^\circ\text{C}$	I_D	200	A
	$T_c = 100^\circ\text{C}$	I_D	126	A
Pulsed Drain Current ²		I_{DM}	800	A
Single Pulsed Avalanche Current ³		I_{AS}	72	A
Single Pulsed Avalanche Energy ³		E_{AS}	1296	mJ
Power Dissipation ⁵	$T_c = 25^\circ\text{C}$	P_D	250	W
Thermal Resistance from Junction to Ambient ⁶		$R_{\theta JA}$	69	$^\circ\text{C}/\text{W}$
Thermal Resistance from Junction to Case		$R_{\theta JC}$	0.5	$^\circ\text{C}/\text{W}$
Junction Temperature		T_J	150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55~+150	$^\circ\text{C}$

MOSFET ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Off Characteristics						
Drain - Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0V, I_D = 250\mu\text{A}$	150			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 150\text{V}, V_{GS} = 0\text{V}$			1	μA
Gate - Body Leakage Current	I_{GSS}	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$			± 100	nA
On Characteristics⁴						
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	2	3	4	V
Drain-source On-resistance	$R_{DS(\text{on})}$	$V_{GS} = 10\text{V}, I_D = 20\text{A}$		3.8	5.0	$\text{m}\Omega$
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS} = 75\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$		8620		pF
Output Capacitance	C_{oss}			779		
Reverse Transfer Capacitance	C_{rss}			24		
Gate Resistance	R_g	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$		5.2		Ω
Switching Characteristics						
Total Gate Charge	Q_g	$V_{DS} = 75\text{V}, V_{GS} = 10\text{V}, I_D = 20\text{A}$		123		nC
Gate-source Charge	Q_{gs}			44		
Gate-drain Charge	Q_{gd}			29		
Turn-on Delay Time	$t_{d(\text{on})}$	$V_{DD} = 75\text{V}, V_{GS} = 10\text{V}, I_D = 20\text{A}, R_G = 2.7\Omega$		26		ns
Turn-on Rise Time	t_r			54		
Turn-off Delay Time	$t_{d(\text{off})}$			101		
Turn-off Fall Time	t_f			55		
Source - Drain Diode Characteristics						
Diode Forward Voltage ⁴	V_{SD}	$V_{GS} = 0\text{V}, I_s = 20\text{A}$			1.2	V

Notes :

- 1.The maximum current rating is limited by package.And device mounted on a large heatsink
- 2.Pulse Test : Pulse Width $\leq 10\mu\text{s}$, duty cycle $\leq 1\%$.
- 3.E_{AS} condition: $V_{DD} = 75\text{V}, V_{GS} = 10\text{V}, L = 0.5\text{mH}, R_G = 25\Omega$ Starting $T_J = 25^\circ\text{C}$.
- 4.Pulse Test : Pulse Width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
- 5.The power dissipation P_D is limited by $T_{J(\text{MAX})} = 150^\circ\text{C}$.And device mounted on a large heatsink
- 6.Device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$.

TO-220-3L-C Package Information


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.300	4.700	0.169	0.185
A1	2.200	2.600	0.087	0.102
b	0.600	1.000	0.024	0.039
b1	1.150	1.600	0.045	0.063
c	0.300	0.700	0.012	0.028
c1	1.000	1.400	0.039	0.055
D	9.600	10.400	0.378	0.409
E	8.800	9.750	0.346	0.384
E1	11.800	13.300	0.465	0.524
e	2.540BSC		0.100BSC	
e1	4.840	5.320	0.191	0.209
F	2.600	3.000	0.102	0.118
L	12.600	14.800	0.496	0.583
L1	2.800	4.200	0.110	0.165
Φ	3.400	4.000	0.134	0.157